

Day 1 - Wednesday February 1, 2023

Description/Title	Presenter	Affiliation
S1: Plenary I : Design for Power		
<i>To be Announced</i>		
Finite-Element Predictive Modeling for Power Modules	<i>Dr. Brandon Passmore</i>	<i>Wolfspeed</i>
Break		
S2: IVR for Computers and Servers		
Session Chair	Dr. Siddharth Ravichandran	Chipletz
Keynote: Practical challenges with advanced IVR solutions for microprocessors	Dr. Michael J. Hill	Intel
3D Power Delivery for High Performance Processors Architecture, Magnetics, and Performance Bottlenecks for 48V-1V CPU VRM	Dr. Noah Sturcken	Ferric, Inc
	Dr. Minjie Chen	Princeton University
<i>Reserved for Submitted Papers</i>		
Lunch		
S3: Multiphysics Design & Tools		
Session Chair	Dr. Rajen Murugan	Texas Instruments
Keynote: Machine-Learning Based optimization tools for multiphysics design of power modules	Prof. Madhavan Swaminathan	Georgia Tech.
Multiphysics System Co-Design Modeling: State-of-the-Art, Challenges, and Opportunities	Dr. Rajen Murugan	Texas Instruments
New Challenges in Transportation Electrification, Powertrain Drives & New Power Electronics Architectures	Dr. Osama Muhammed	Florida International University
<i>Reserved for Submitted Papers</i>		
Break		
S4: Additive Manufacturing		
Session Chair	Dr. Peter Friedrichs	Infineon
Keynote: Unlocking New Design Frontiers for Power Density and Specific Power Using Heterogenous Packaging, Embedded Cooling, and 3D Printed PCBs	Dr. Thomas Foulkes	Pacergy
Additive deposition technologies: from 2D towards 3D electronic systems	Dr. M. Wiemer	Fraunhofer Institute
<i>Reserved for Submitted Papers</i>		
<i>Reserved for Submitted Papers</i>		
S5: Manufacturing Technologies		
Session Chair	Jason Rouse	Taiyo America
<i>To be Announced</i>	Dr. Matt Kelly	IPC
<i>To be Announced</i>	Dr. Bill Chen	ASE
Enabling Increased Aluminum Utilization within Power Electronic Packaging	Divyakant P Kadiwala	Averatek
<i>To be Announced</i>		
WELCOME RECEPTION		

Day 2 - Thursday February 2, 2023

Description/Title	Presenter	Affiliation
S6: Plenary II: Improved Electrical Performance for Power		
PCB based Integrated Magnetics Superior heat dissipation by low pressure Ag sinter joining and real time AI lifetime prediction for SiC power module	Prof. Fred C. Lee	Virginia Tech.
	Prof. Katsuaki Sukanuma	University of Osaka
Break		

Day 2 - Thursday February 2, 2023 (cont'd)

Description/Title	Presenter	Affiliation
S7: Materials I Interconnects & Lead Attachments		
<i>Session Chair</i>	Andy Mackie	Indium Corporation
Keynote: Cu Sintering/Interconnect Analysis	Prof. Gordon Elger	Fraunhofer Application Center
Keynote: Die Top System: Advanced interconnect for Power Electronics Module Packaging	Dr. Habib Mustain	Heraeus
<i>Reserved for Submitted Papers</i>		
<i>Reserved for Submitted Papers</i>		
Lunch with Posters		
S8: Materials II Substrates & Encapsulants		
<i>Session Chair</i>	Dr. Ninad Shahane	Texas Instruments
Keynote: A Polymer-Nanoparticle Composite for Nonlinear Resistive Field-Grading in Medium-Voltage Power Modules	Prof. G. Q. Lu	Virginia Tech.
<i>Reserved for Submitted Papers</i>		
<i>Reserved for Submitted Papers</i>		
Break		
S9: High Power Module Integration		
<i>Session Chair</i>	Cyril Buttay	
US Navy Power Electronics needs	Dr. L. J. Petersen	US Navy
To be Announced	Dr. S. Idaka	Mitsubishi
<i>Reserved for Submitted Papers</i>		
S11: Thermal Management and Reliability		
<i>Session Chair</i>	Patrick McCluskey	University of Maryland
To be Announced		
To be Announced	Steve Martell	Sonoscan
<i>Reserved for Submitted Papers</i>		
S10: Partners Session		
BANQUET DINNER		

Day 3 - Friday February 3, 2023

Description/Title	Presenter	Affiliation
S14: Plenary III: Improved Electrical Performance for Power		
Emerging Power electronics packaging and system integration for automotive applications	Dr. Mahadevan Iyer	Amkor
To be Announced		
Break		
S12: Passive Component Integration		
<i>Session Chair</i>	John Bultitude	KEMET
Keynote: Enabling Sustainable Power Electronics Through Miniaturization and Integration of Power Magnetics	Dr. Matt Wilkowski	Enachip
Application of Circuit Board Technology for Passives Packaging	B. K. Summey	KEMET
High-density nanoporous silicon decoupling capacitors	Dr. Mohamed Jatlaoui	Murata
<i>Reserved for Submitted Papers</i>		
Lunch		
S13: Low Power & Telemetry		
<i>Session Chair</i>	Prof. Shubhendu Bhardwaj	Florida International University
Keynote: eSiP (energy source in package) - 3D packaging considerations for self-powered IoT edge devices	Dr. Mike Hayes	Tyndall
To be Announced	Sakhrat Khizoev	University of Miami
<i>Reserved for Submitted Papers</i>		
<i>Reserved for Submitted Papers</i>		
Break		
S15: LABORATORY TOUR - Florida International University		