

3D PEIM 2023 Preliminary Program

Day 1 - Wednesday February 1, 2023

Sessions	Description/Title	Presenter	Affiliation
S1: Plenary I : Design for Power	<i>Highly integrated low inductance SiC power modules</i>	<i>Dr. Michael Guyenot</i>	<i>Bosch</i>
	<i>Finite-Element Predictive Modeling for Power Modules</i>	<i>Dr. Brandon Passmore</i>	<i>Wolfspeed</i>
Break			
S2: IVR for Computers and Servers	<i>Session Chair</i>	<i>Dr. Siddharth Ravichandran</i>	<i>Chipletz</i>
	<i>Keynote: Practical challenges with advanced IVR solutions for microprocessors</i>	<i>Dr. Michael J. Hill</i>	<i>Intel</i>
	<i>3D Power Delivery for High Performance Processors</i>	<i>Dr. Noah Sturcken</i>	<i>Ferric, Inc</i>
	<i>Architecture, Magnetics, and Performance Bottlenecks for 48V-1V CPU VRM</i>	<i>Dr. Minjie Chen</i>	<i>Princeton University</i>
	<i>Reserved for Submitted Papers</i>		
Lunch			
S3: Multiphysics Design & Tools	<i>Session Chair</i>	<i>Dr. Rajen Murugan</i>	<i>Texas Instruments</i>
	<i>Keynote: Machine-Learning Based optimization tools for multiphysics design of power modules</i>	<i>Prof. Madhavan Swaminathan</i>	<i>Georgia Tech.</i>
	<i>Multiphysics System Co-Design Modeling: State-of-the-Art, Challenges, and Opportunities</i>	<i>Dr. Rajen Murugan</i>	<i>Texas Instruments</i>
	<i>New Challenges in Transportation Electrification, Powertrain Drives & New Power Electronics Architectures</i>	<i>Dr. Osama Muhammed</i>	<i>Florida International University</i>
	<i>Reserved for Submitted Papers</i>		
Break			
S4: Additive Manufacturing	<i>Session Chair</i>	<i>Dr. Peter Friedrichs</i>	<i>Infineon</i>
	<i>Packaging, Embedded Cooling, and 3D Printed PCBs</i>	<i>Dr. Thomas Foulkes</i>	<i>Pacergy</i>
	<i>Additive deposition technologies: from 2D towards 3D electronic systems</i>	<i>Dr. M. Wiemer</i>	<i>Fraunhofer Institute</i>
	<i>Reserved for Submitted Papers</i>		
	<i>Reserved for Submitted Papers</i>		
S5: Manufacturing Technologies	<i>Session Chair</i>	<i>Jason Rouse</i>	<i>Taiyo America</i>
	<i>To be Announced</i>	<i>Dr. Matt Kelly</i>	<i>IPC</i>
	<i>To be Announced</i>	<i>Dr. Bill Chen</i>	<i>ASE</i>
	<i>Enabling Increased Aluminum Utilization within Power Electronic Packaging</i>	<i>Divyakant P Kadiwala</i>	<i>Averatek</i>
	<i>To be Announced</i>		
WELCOME RECEPTION			
S6: Plenary II: Improved Electrical Performance for Power	<i>PCB based Integrated Magnetics power module</i>	<i>Prof. Fred C. Lee</i>	<i>Virginia Tech.</i>
		<i>Prof. Katsuaki Suganuma</i>	<i>University of Osaka</i>
Break			
S7: Materials I Interconnects & Lead Attachments	<i>Session Chair</i>	<i>Andy Mackie</i>	<i>Indium Corporation</i>
	<i>Keynote: Cu Sintering/Interconnect Analysis</i>	<i>Prof. Gordon Elger</i>	<i>Fraunhofer Application Center</i>
	<i>Keynote: Die Top System: Advanced interconnect for Power Electronics Module Packaging</i>	<i>Dr. Habib Mustain</i>	<i>Heraeus</i>
	<i>Reserved for Submitted Papers</i>		
	<i>Reserved for Submitted Papers</i>		
Lunch with Posters			
S8: Materials II Substrates & Encapsulants	<i>Session Chair</i>	<i>Dr. Ninad Shahane</i>	<i>Texas Instruments</i>
	<i>Voltage Power Modules</i>	<i>Prof. G. Q. Lu</i>	<i>Virginia Tech.</i>
	<i>Reserved for Submitted Papers</i>		
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Break			
S9: High Power Module Integration	<i>Session Chair</i>	<i>Cyril Buttay</i>	
	<i>US Navy Power Electronics needs</i>	<i>Dr. L. J. Petersen</i>	<i>US Navy</i>
	<i>To be Announced</i>	<i>Dr. S. Idaka</i>	<i>Mitsubishi</i>
	<i>Reserved for Submitted Papers</i>		
S11: Thermal Management and Reliability	<i>Session Chair</i>	<i>Patrick McCluskey</i>	<i>University of Maryland</i>
	<i>To be Announced</i>		
	<i>To be Announced</i>	<i>Steve Martell Sonoscan NDA 3D</i>	<i>Sonoscan</i>
	<i>Reserved for Submitted Papers</i>		
S10: Partners Session	<i>To be Announced</i>		
BANQUET DINNER			
S14: Plenary III: Improved Electrical Performance for Power	<i>Emerging Power electronics packaging and system integration for automotive applications</i>	<i>Dr. Mahadevan Iyer</i>	<i>Amkor</i>
	<i>To be Announced</i>		
Break			
S12: Passive Component Integration	<i>Session Chair</i>	<i>John Bultitude</i>	<i>KEMET</i>
	<i>Keynote: Enabling Sustainable Power Electronics Through Miniaturization and Integration of Power Magnetics</i>	<i>Dr. Matt Wilkowski</i>	<i>Enachip</i>
	<i>Application of Circuit Board Technology for Passives Packaging</i>	<i>B. K. Summey</i>	<i>KEMET</i>
	<i>High-density nanoporous silicon decoupling capacitors</i>	<i>Dr. Mohamed Jatlaoui</i>	<i>Murata</i>
	<i>Reserved for Submitted Papers</i>		
Lunch			
S13: Low Power & Telemetry	<i>Session Chair</i>	<i>Prof. Shubhendu Bhardwaj</i>	<i>Florida International University</i>
	<i>Keynote: eSiP (energy source in package) - 3D packaging considerations for self-powered IoT edge devices</i>	<i>Dr. Mike Hayes</i>	<i>Tyndall</i>
	<i>To be Announced</i>	<i>Sakhrat Khizoov</i>	<i>University of Miami</i>
	<i>Reserved for Submitted Papers</i>		
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Break			
S15: LABORATORY TOUR	<i>Florida International University</i>		