

**Day 1 - Wednesday February 1, 2023**

Description/Title	Presenter	Affiliation
<b>S1: Plenary I : Design for Power</b>		
<i>Finite-Element Predictive Modeling for Power Modules</i>	<i>Dr. Brandon Passmore</i>	<i>Wolfspeed</i>
<i>Integrated Power Delivery for AI Computing: Technology Gaps &amp; Opportunities</i>	<i>Prof. Madhavan Swaminathan</i>	<i>Georgia Tech.</i>
<b>Break</b>		
<b>S2: IVR for Computers and Servers</b>		
<i>Session Chair</i>	Siddharth Ravichandran	Chiptetz
<i>Keynote: Practical challenges with advanced IVR solutions for microprocessors</i>	Michael J. Hill	Intel
3D Power Delivery for High Performance Processors	Noah Sturcken	Ferric, Inc
Topology and Magnetics Co-Investigation for 48V-1V Point-of-Load VRM	Minjie Chen	Princeton University
Inductor-Linked Multi-Output Chiptet Power Delivery Architecture	Mian Liao, Ping Wang, Minjie Chen	Princeton University
<b>Lunch</b>		
<b>S3: Multiphysics Design &amp; Tools</b>		
<i>Session Chair</i>	Rajen Murugan	Texas Instruments
<i>Keynote: Machine-learning-based optimization: the future of power package design</i>	Vanessa Smet	Georgia Tech.
<i>Keynote: Multiphysics System Co-Design Modeling: State-of-the-Art, Challenges, and Opportunities</i>	Rajen Murugan	Texas Instruments
<i>New Challenges in Transportation Electrification, Powertrain Drives &amp; New Power Electronics Architectures</i>	Osama Muhammed	Florida International University
<i>Performance Analysis of Thin- and Thick-Film Graphene-Based EMI Shields in Integrated Power Modules</i>	Ghaleb Al Duhni (John Volakis, Markondeyaraj Pulugurtha)	Florida International University
<i>Reliability Analysis of Wireless Power Transfer for Electric Vehicle Charging Based on Continuous Markov Process</i>	Arif Sarwat (Milad Behnamfar, Md Abu Taher, Alexis Polowsky, Sukanta Roy)	Florida International University
<b>Break</b>		
<b>S4: Additive Manufacturing</b>		
<i>Session Chair</i>	Peter Friedrichs	Infineon
<i>Keynote: Nano Additive Manufacturing of Challenging Materials</i>	Wendy Gu	Stanford University
<i>Additive deposition technologies: from 2D towards 3D electronic systems</i>	M. Wiemer	Fraunhofer Institute, Germany
<i>Additive manufacturing of high performance pure copper components with PureForm™ for thermal and electrical applications</i>	Arian Aghababaie	Holo, Inc.
<b>S5: Manufacturing Technologies</b>		
<i>Session Chair</i>	Jason Rouse	Taiyo America
<i>Keynote: Insights from Microelectronic Packaging for Power Packaging Advancement</i>	Matt Kelly	IPC
<i>Vertical Integrated High Density Power Packaging Technology Enabling Increased Aluminum Utilization within Power Electronic Packaging</i>	C.P. Hung, Matt Li, Kay Essig	ASE
	Divyakant P Kadiwala	Averatek
<i>Reticular Graphene Reinforced Copper for Low-Stress Thermal Management Application</i>	Ambreem Nisar (Cheng Zhang, Markondeyaraj Pulugurtha, Arvind Agarwal, Al Dumni Ghaleb)	Florida International University
<b>WELCOME RECEPTION</b>		

**Day 2 - Thursday February 2, 2023**

Description/Title	Presenter	Affiliation
<b>S6: Plenary II: Improved Electrical Performance for Power</b>		
<i>PCB based Integrated Magnetics</i>	<i>Prof. Fred C. Lee</i>	<i>Virginia Tech.</i>
<i>Future of Packaging and the Role of Power Integration</i>	<i>Prof. Rao R. Tummala</i>	<i>3D Electronic Systems Packaging Research Center(PRC)</i>
<b>Break</b>		

**Day 2 - Thursday February 2, 2023 cont'd**

Description/Title	Presenter	Affiliation
<b>S7: Materials I Interconnects &amp; Lead Attachments</b>		
<i>Session Chair</i>	Andy Mackie	Indium Corporation
<i>Keynote: Roadmap for Copper Sintering - Next Interconnect for Power Electronic Module Packaging</i>	Gordon Elger	Fraunhofer Application Center
<i>Keynote: Die Top System: Advanced interconnect for Power Electronics Module Packaging</i>	Habib Mustain	Heraeus
Rapid Development of Electrically Conductive Materials for Additive Manufacturing Feasibility and Applications	Gilad Nave, Patrick McCluskey	University of Maryland
<b>S8: Materials II Substrates &amp; Encapsulants</b>		
<i>Session Chair</i>	Ninad Shahane	Texas Instruments
<i>Keynote: A Polymer-Nanoparticle Composite for Nonlinear Resistive Field-Grading in Medium-Voltage Power Modules</i>	G. Q. Lu	Virginia Tech.
New Substrate Technology for Power Applications	Neda Ameli	Rogers Corporation
<i>Lunch with Posters</i>		
<b>S9: Module Integration</b>		
<i>Session Chair</i>	Vanessa Smet	Georgia Tech.
<i>Keynote: Heterogeneous Roadmap Update</i>	Patrick McCluskey	University of Maryland
Towards next generation power module package technology blooming	S. Idaka	Mitsubishi Electric R&D Centre Europe
An intelligent power module	Julien Morand, Johan Le Lesle	Mitsubishi Electric R&D Centre Europe
WAVELET-Based Parameter Estimation of High Voltage Impulse Signals	Emel Onal	Istanbul Technical University
NMOS/NLDMOS LSS dead-Time Minority Carrier Isolation Optimization	Gang Liu, Olivier Causse	onsemi
<i>Break</i>		
<b>S11: Thermal Management and Reliability</b>		
<i>Session Chair</i>	Patrick McCluskey	University of Maryland
<i>Keynote: integrated thermal management in power electronics and motors</i>	Satish Kumar	Georgia Tech.
Power Electronics Thermal Design with Carbice Nanotubes for Reliability and Cost Saving	Craig Green	Carbice Corporation
Inverter/Converter Power Density and Flexibility Improvements Through Modularity and Novel Thermal Management Architecture	Ian Byers	Marel Power Solutions, Inc.
<b>S10: Partners Session</b>		
Amkor Technology		
KEMET Electronics Corporation		
FIU Biomedical Engineering		
Indium Corporation		
POSTERS & BENCHTOP PARTNERS		
<b>BANQUET DINNER</b>		

**Day 3 - Friday February 3, 2023**

Description/Title	Presenter	Affiliation
<b>S14: Plenary III: Improved Electrical Performance for Power</b>		
<i>Superior heat dissipation by low pressure Ag sinter joining and real time AI lifetime prediction for SiC power module</i>	<i>Prof. Katsuaki Suganuma</i>	<i>University of Osaka, Japan</i>
<i>Emerging Power electronics packaging and system integration for automotive applications</i>	<i>Dr. Mahadevan Iyer</i>	<i>Amkor</i>
<i>Break</i>		

**Day 2 - Friday February 3, 2023 cont'd**

Description/Title	Presenter	Affiliation
<b>S12: Passive Component Integration</b>		
<i>Session Chair</i>	John Bultitude	KEMET Electronics Corporation
<b>Keynote: Enabling Sustainable Power Electronics Through Miniaturization and Integration of Power Magnetics</b>	Matt Wilkowski	Enachip
Application of Circuit Board Technology for Passives Packaging	B. K. Summey	KEMET Electronics Corporation
High-density nanoporous silicon decoupling capacitors	Mohamed Jatlaoui	Murata Integrated Passive Solutions, France
Cold-sprayed aluminum capacitors for 3D power packaging	Reshmi Banerjee (Denny John, Cheng Zhang, Arvind Agarwal, P. Markondeya Raj)	Florida International University
Class I Multi-Layer Ceramic Capacitors (MLCCs) Performance As Wide Band Gap (WBG) Snubbers for Hard Switching Applications	Allen Templeton, Nathan Reed, Hunter Hayes, James Davis, John Bultitude	KEMET Electronics Corporation
<b>Lunch</b>		
<b>Lunch Plenary: Magnetolectric Nanoparticles As a Wireless Brain-Machine Interface</b>	Sakhrat Khizoev	University of Miami
<b>S13: Low Power &amp; Telemetry</b>		
<i>Session Chair</i>	Girish Wable	Jabil
<b>Keynote: eSiP (energy source in package) - 3D packaging considerations for self-powered IoT edge devices</b>	Brian Zahnstecher	PowerRox
Manufacturing Challenges and Qualification of 3D Packaging	Chuck Woychik	Skywater Technology
Metamaterial beam steering for wireless power	Hae-In Kim, Y.K. Yoon	University of Florida
Laminate-Embedded Multimodal Energy Harvester for Multilevel Power Supply	Jorge A. Caripidis Troccola (Sweta Gupta, Maxence Carvalho, Satheesh Bojja Venkatakrishnan, Pulugurtha Markondey Raj)	Florida International University
<b>Break</b>		
<b>S15: LABORATORY TOUR - Florida International University</b>		
<b>Posters</b>		
Laser-Induced graphene supercapacitors on flex substrates for package-integrated Power Supply in Wearable and IoT Devices	Reshmi Banerjee, Azmal Chowdhury, Pavar Sai Kumar, Chunlei Wang, Sanket Goel, Pulugurtha Markondeya Raj	Florida International University
Copper nanowired Interconnection for Embedding Power Dies in PCB	Caio De Oliveira Mendes, Julien Morand, Vincent Bley, Jean-Pascal Cambonne, Guillaume Lefevre	Mitsubishi Electric R&D Center Europe
New Design Concepts for PCB-Integration Technology in Power Electronics Reducing Circuit Parasitics to a Minimum	Rando Raßmann, Jasper Schnack, Ulf Schumann	University of Applied Science Kiel
Low Frequency Power Telemetry Using Multiferroic Laminate Heterostructures	Pawan Gaire, Veeru Jaiswal, Markondeyaraj Pulugurtha, Maria Thuy Do, Ravi Mullapudi, Shubhendu Bhardwaj	University of Nebraska-Lincoln
Overview of Power Electronic Converters in Electric Vehicle Applications	S M Sajjad Hossain Rafin, Rejaul Islam, Osama Mohammed	Florida International University
Wide Band Gap Semiconductor Devices for Power Electronic Converters	S M Sajjad Hossain Rafin, Rony Ahmed, Osama Mohammed	Florida International University
A Review of Power Electronics Converters for Electronic Aircrafts	S M Sajjad Hossain Rafin, Md Ashikul Haque, Osama Mohammed	Florida International University
Power Electronic Converters for Wind Power Generation	S M Sajjad Hossain Rafin, Rejaul Islam, Osama Mohammed	Florida International University